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Docket No.

BUR920030118US1 (IBU-10-5799)

Applicant(s):

Gill et al

Serial No.

Filing Date

Examiner

Group Art Unit

10/708,023

February 3, 2004

Kevin Ouinto

<u> 2826</u>

Invention:

STRUCTURE AND METHOD FOR LOCAL RESISTOR ELEMENT IN INTEGRATED CIRCUIT **TECHNOLOGY**

I hereby certify that this Amendment is being transmitted via facsimile to the United States Patent and Trademark Office

Fax. No. <u>703-872-9306</u> January 27, 2005 (Date) (No. of pages)

TO:

Examiner Quinto

The fee has been calculated as shown below:

(Col. 1)		(Col. 2)	(Col. 3)		
CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA		
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☐ 1ST PRESENTATION OF MULTIPLE DEPENDENT CLAIMS					

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Gill et al) Examiner: Kevin Q	uinto
Serial No. 10/708,023) Art Unit: 2826	
Filed: February 3, 2004)) Confirmation No. 20	022
For: STRUCTURE AND METHOD FOR LOCAL RESISTOR ELEMENT IN INTEGRATED CIRCUIT TECHNOLOGY)))	
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Attorney Docket No.: BUR920030118US1 (IBU-10-5799)

AMENDMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Responsive to the Examiner's letter of November 3, 2004, it is respectfully requested that the above entitled application be amended as shown on the attached sheets.